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ANTONELLI, TERRY STOUT & KRAUS, LLP
 1300 NORTH SEVENTEENTH STREET
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 ARLINGTON, VA 22209-3873

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Ricardo E. Pares (Depositor's name)

Ricardo Pares (Signature)

12/07/2006 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/500,931

07/08/2004

Hiroaki Kikuchi

1374.43797X00

1570

TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE MANUFACTURING METHOD

APPL. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE (S) DUE	DATE DUE
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Nonprovisional

NO

\$1400

\$300

\$1700

12/07/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
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ISAAC, STANETTA D

2812

438-149000

12/07/2006 CNGUYEN1 00000000 10500931

1. Change of correspondence address or indication of "Fee Address": (37 CFR 1.363).

☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.

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2. For printing on the patent front page, list

01 FC:1501

1400.00 OP

(1) the names of up to 3 registered patent attorneys

1 ANTONELLI, TERRY, STOUT

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Or agents OR, alternatively,

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6.00 OP

(2) the name of single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed no name will be printed.

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PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

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RENESAS TECHNOLOGY CORP.
 HITACHI ULSI SYSTEMS CO., LTD.
 RENESAS NORTHERN JAPAN SEMICONDUCTOR, INC.

TOKYO, JAPAN
 TOKYO, JAPAN
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☐

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☒

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☐

a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.

☐

b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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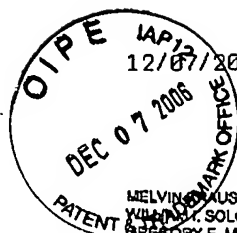
Date: DECEMBER 7, 2006

Typed or printed name: William L Solomon

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This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450. Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

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FACSIMILE TRANSMITTAL SHEET
DECEMBER 7, 2006

TO: Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RE: U.S. Patent Application No. **10/500,931**
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE MANUFACTURING
METHOD
Filing Date: 07/08/2004
Atty. Docket No. **1374.43797X00**

From: William I. Solomon

Fax Number: (571) 273-2885

Total Number of Pages: **3**

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I hereby certify that on DECEMBER 7, 2006, the above-identified documents are being facsimile transmitted to the United States Patent and Trademark Office.

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Name (Print)

Ricardo Perez

Signature